

EAST - [1015267.wsp.1]

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DB: USPAT/US\_PGPUB/EPD/JPO/DERWEAT/ISM/TDS Default operator: OR

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(10) ((reference adj2 (volt\$3 potential bias current)) with distance) same delay  
 (19) ((memory and clock\$3 and ((first near3 reference near3 (volt\$3 potential bias current)) with distance) same delay)  
 (35) ((reference adj2 (volt\$3 potential bias current)) with distance) same delay  
 (17) (reference adj2 (volt\$3 potential bias current)) with distance with delay\$3  
 (24) ((reference adj2 (volt\$3 potential bias current)) with distance) and (memory and clock\$3 and ((first near3 reference near3 (volt\$3 potential bias current)) with distance) same delay  
 (4) ((memory and clock\$3 and ((first near3 reference near3 (volt\$3 potential bias current)) with distance) same delay)  
 (13740) (first near3 reference near3 (volt\$3 potential bias)) and (second near3 reference near3 (volt\$3 potential bias))  
 (846) (memory and clock\$3 and ((first near3 reference near3 (volt\$3 potential bias)) with distance) same delay  
 (63) ((memory and clock\$3 and ((first near3 reference near3 (volt\$3 potential bias)) with distance) same delay)  
 (3) ((memory and clock\$3 and ((first near3 reference near3 (volt\$3 potential bias)) with distance) same delay)  
 (1146) (reference adj2 (volt\$3 potential bias current)) with distance  
 (271) ((memory and clock\$3 and ((first near3 reference near3 (volt\$3 potential bias)) with distance) same delay)  
 (2) ((reference adj2 (volt\$3 potential bias current)) with distance) and (((memory and clock\$3 and ((first near3 reference near3 (volt\$3 potential bias)) with distance) same delay)  
 (1603) memory and clock\$3 and ((first near3 reference near3 (volt\$3 potential bias)) with distance) same delay  
 (4) ((reference adj2 (volt\$3 potential bias current)) with distance with delay\$3) and (((memory and clock\$3 and ((first near3 reference near3 (volt\$3 potential bias)) with distance) same delay)  
 (168) 5,263,000 5,473,198 5,548,560 5,757,705 5,933,379 5,991,232 6,055,209 6,166,989  
 (16) US-5263000-\$ DID. OR US-5473198-\$ DID. OR US-5548560-\$ DID. OR US-5757705-\$ DID.  
 (0) (US-5263000-\$ DID. OR US-5473198-\$ DID. OR US-5548560-\$ DID. OR US-5757705-\$ DID.  
 (1) (US-5263000-\$ DID. OR US-5473198-\$ DID. OR US-5548560-\$ DID. OR US-5757705-\$ DID.

Document ID Issue Date Pages Title Current OR Current XRef Retrieval C Inventor S C P I

1	<input type="checkbox"/> US 20020190255 A 20021219 43 Semiconductor integrated circuit device	257/69			Kitahara, Terumasa et al.	<input checked="" type="checkbox"/>				
2	<input type="checkbox"/> US 5841093 A 19981124 18 Electrical discharge apparatus and electrical apparatus	219/69.17	219/69.13; 219/69.14;		Wada, Mitsuyoshi	<input checked="" type="checkbox"/>				
3	<input type="checkbox"/> US 6057910 A 20000502 21 Self-calibrating precision timing circuit and method	356/5.05	342/203; 356/28;		Dunne, Jeremy G.	<input checked="" type="checkbox"/>				
4	<input type="checkbox"/> US 6411160 B1 20020625 18 Semiconductor integrated circuit device	327/565	257/223.079; 257/223.144		Riho, Yoshiro et al.	<input checked="" type="checkbox"/>				

Details HTML

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